



Docket No.: 060188-0075

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor Application of	:	Customer Number: 20277
Makoto FUJIWARA	:	Confirmation Number: 5700
Application No.: 09/867,766	:	Group Art Unit: 2134
Filed: May 31, 2001	:	Examiner: Poltorak, Piotr

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING  
SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT UNDER 37 C.F.R. §1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to Office Action dated November 4, 2004, having a three-month shortened statutory period for response set to expire February 4, 2005, a petition for a two-month extension of time up to and including April 4, 2005, being filed concurrently herewith, please amend the above-identified application as follows: